TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of
Invention

METHOD FOR FABRICATING INTEGRATED CIRCUITS HAVING BOTH HIGH VOLTAGE AND LOW VOLTAGE DEVICES

Application Number:

Date:

First Named Applicant:

Jung-Ching CHEN

Confirmation Number:

Attorney Docket Number:

NAUP0575USA

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Registered Number: 41,526	777

Decuments being submitted:	Files
Documents being submitted:	Files
us-power-of-attorney-grant	NAUP0575_0_A1-uspoat.xml
 	us-power-of-attorney-grant.dtd
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us-assignment	NAUP0575_0_A1-usassn.xml
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us-declaration	NAUP0575_0_A1-usdecl.xml
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Comments		